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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/606,454	06/26/2003	Brian M. Kerrigan	RPS920030049US1	2960
47052	7590	05/31/2005	EXAMINER	
SAWYER LAW GROUP LLP			BUI, HUNG S	
PO BOX 51418			ART UNIT	
PALO ALTO, CA 94303			PAPER NUMBER	
			2841	

DATE MAILED: 05/31/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)	
	10/606,454	KERRIGAN ET AL.	
	Examiner	Art Unit	
	Hung S. Bui	2841	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on ____.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-21 is/are pending in the application.
- 4a) Of the above claim(s) ____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) ____ is/are allowed.
- 6) ☒ Claim(s) 1-21 is/are rejected.
- 7) ☐ Claim(s) ____ is/are objected to.
- 8) ☐ Claim(s) ____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 10/31/2003 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. ____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. ____. |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| Paper No(s)/Mail Date <u>06/26/2003</u> . | 6) <input type="checkbox"/> Other: ____. |

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 1-21 are rejected under 35 U.S.C. 102(b) as being anticipated by Wallace [US 4,445,740].

Regarding claims 1, 7 and 9, Wallace discloses a dual motion docking apparatus (figures 1-2) for docking an electronics console to a component board in a chassis (3), wherein the electronics console must be mated with the component board (figure 1) at an angle orthogonal to a direction of installation of the electronics console, comprising:

- a first docking mechanism for slidably inserting the electronics console (1) into the chassis (vertical direction), such that at least one connector (9) on the electronic console align with connector (figure 1) on the component board; and
- a second docking mechanism for laterally moving the electronics console towards the component board (horizontal direction), such that the electronics console connectors engage the component board connectors, thereby enabling the blind docking between the electronics console and the component board (figure 2).

Regarding claim 2, Wallace discloses the first docking mechanism comprises a docking base (15) having a longitudinal female portion (a channel on the docking base,

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figure 1), and a longitudinal male portion (an edge of the console board) located on an underside of the electronic console, wherein when the electronic console is inserted into an opening in a chassis, the male portion engages the female portion of the docking base to guide the electronics console along the docking base as the electronic console is slid into the chassis (figure 1).

Regarding claim 3, Wallace discloses the electronic console includes a base plate (a side wall of the chassis 3) that includes at least one key slot (13), and the docking base includes at least one key, wherein after the electronic console is slid into the chassis, respective keys are aligned with respective key slots (figure 2).

Regarding claim 4, Wallace further discloses the keys slots on the base plate mate with the keys of the base plate to ensure that the electronic console connectors correctly align with the component board connectors when the electronic console is in a final docked position (figure 2).

Regarding claims 5-6, Wallace further discloses a handle (11) extending from a front of the electronics console with a cam mechanism (19), wherein the cam mechanism pushes the electronic console matting with the component board (figure 2) by means a cam track (21).

Regarding claim 8, Wallace discloses the electronic console further includes retractable lateral support members in a side opposite the component bard, wherein when the electronic console is in an undocked position, the lateral support members are retracted within the side of the electronic console, and when the electronics console is

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in a final docked position, the lateral support members extend from the side to restrain the electronic console both vertical and horizontal directions (figures 2-1).

Regarding claim 10, Wallace further discloses the electronic console comprising a central electronic console (figures 1, 2 and 8).

Regarding claims 11-21, the claim method steps are inherent in the product structure.

Conclusion

3. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

- Liu [US 6,821,141] discloses sliding track assembly for module PC board;
- Someno [US 5,325,269] discloses apparatus having plurality of plug-in packages detachably housed in housing thereof;
- Bailis et al. [US 6,053,760] disclose universal device for mounting circuit cards in a computer or like electrical machine;
- Bailis et al. [US 6,373,712] disclose device for inserting circuit cards into electrical machines;
- Barthel et al. [US 5,868,585] disclose system and method for inserting circuit boards in tight spaces; and
- Hillis et al. [US 5,317,481] disclose a circuit board and insertion tool.

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4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Hung S. Bui whose telephone number is (571) 272-2102. The examiner can normally be reached on Monday-Friday 8:30AM-6:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kamand Cuneo can be reached on (571) 272-1957. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

5/26/05
Hung Bui
Ex. Art unit 2841



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